

FIG. 1  
PRIOR ART

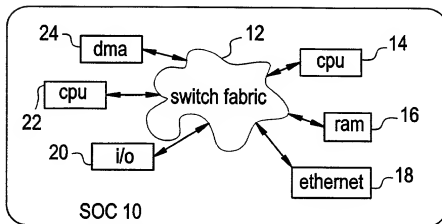


FIG. 2

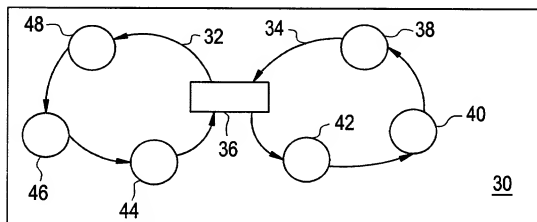


FIG. 3

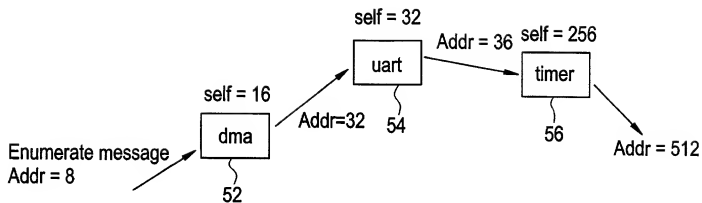


FIG. 4

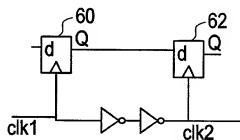
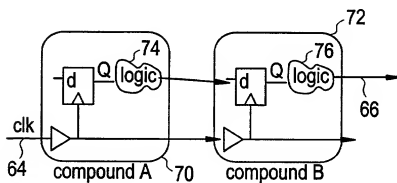


FIG. 5



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FIG. 6

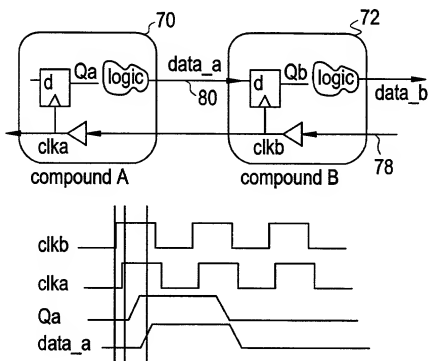


FIG. 7

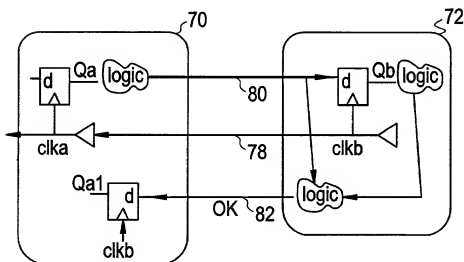


FIG. 8

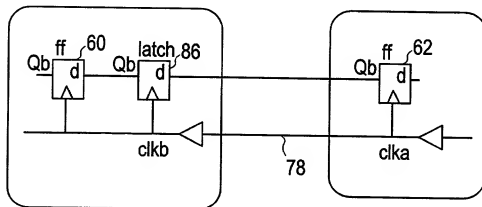
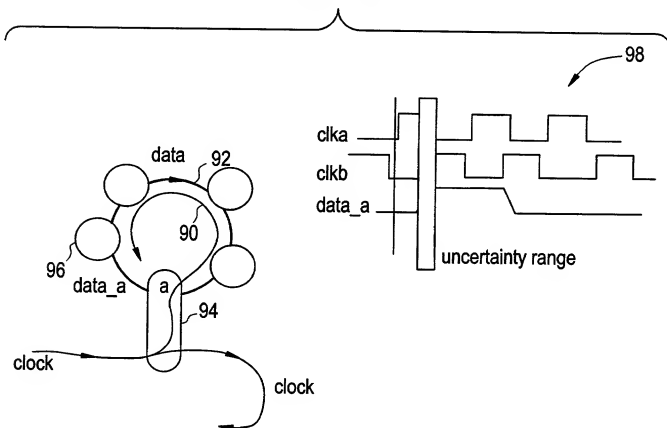


FIG. 9



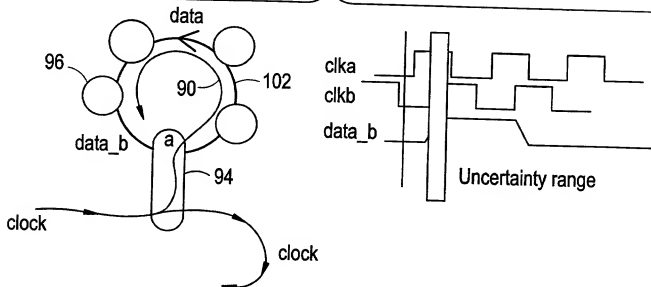


FIG. 11

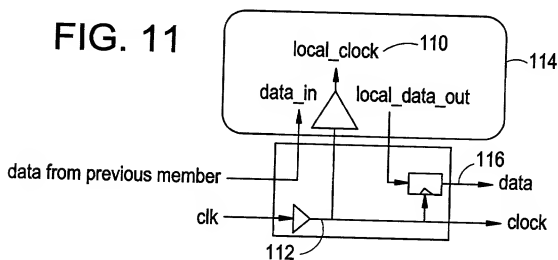


FIG. 12

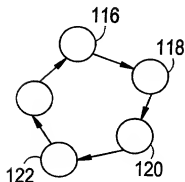


FIG. 13

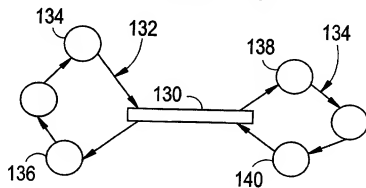


FIG. 14

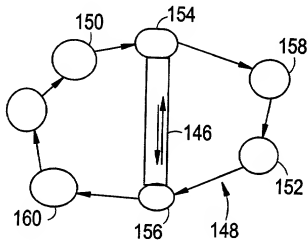


FIG. 15

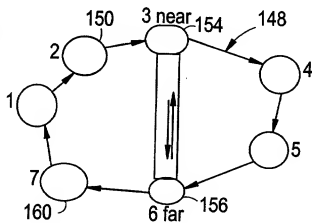


FIG. 16

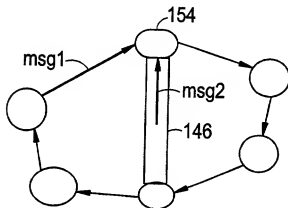


FIG. 17

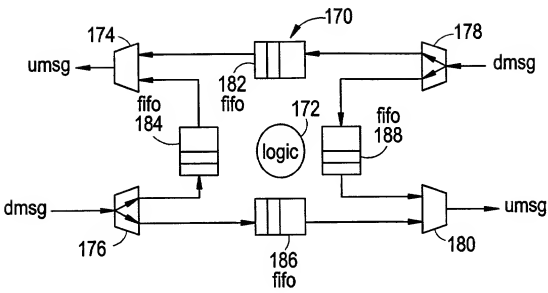


FIG. 18

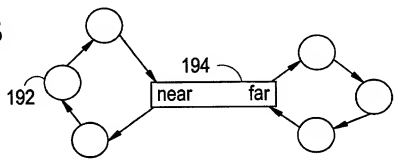


FIG. 19

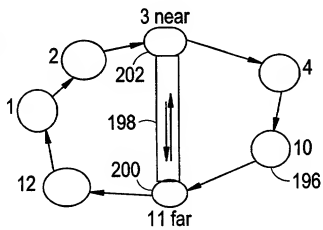


FIG. 20

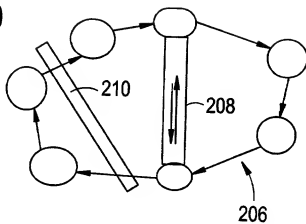


FIG. 21

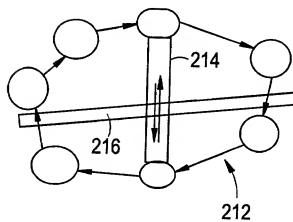


FIG. 22

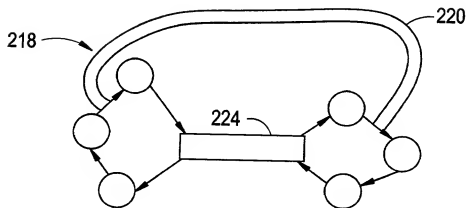




FIG. 23

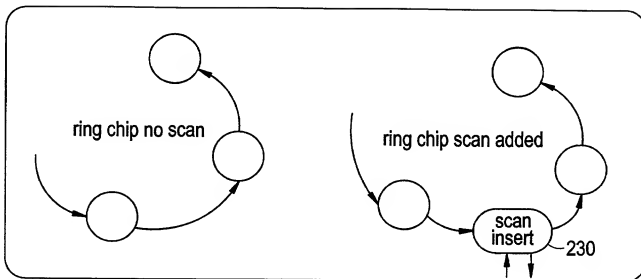


FIG. 24

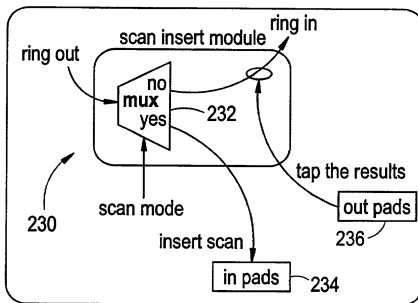


FIG. 25

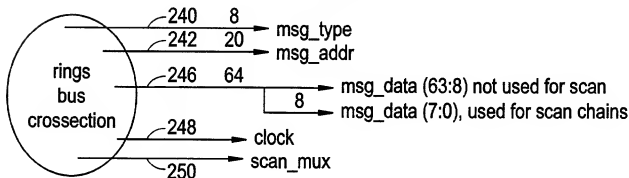


FIG. 26

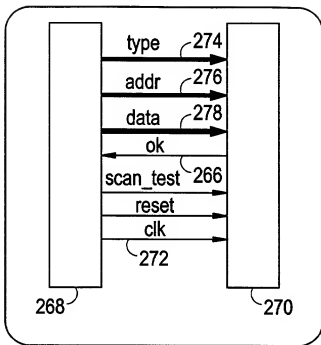
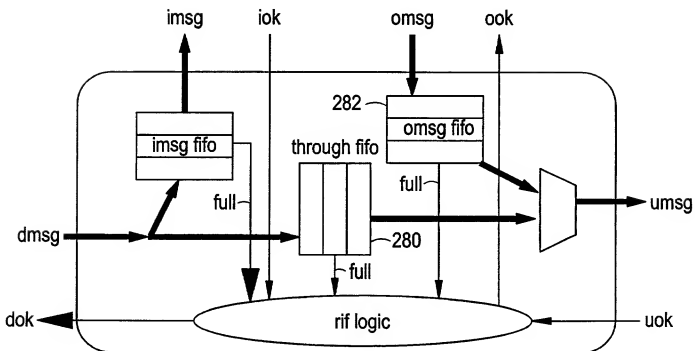
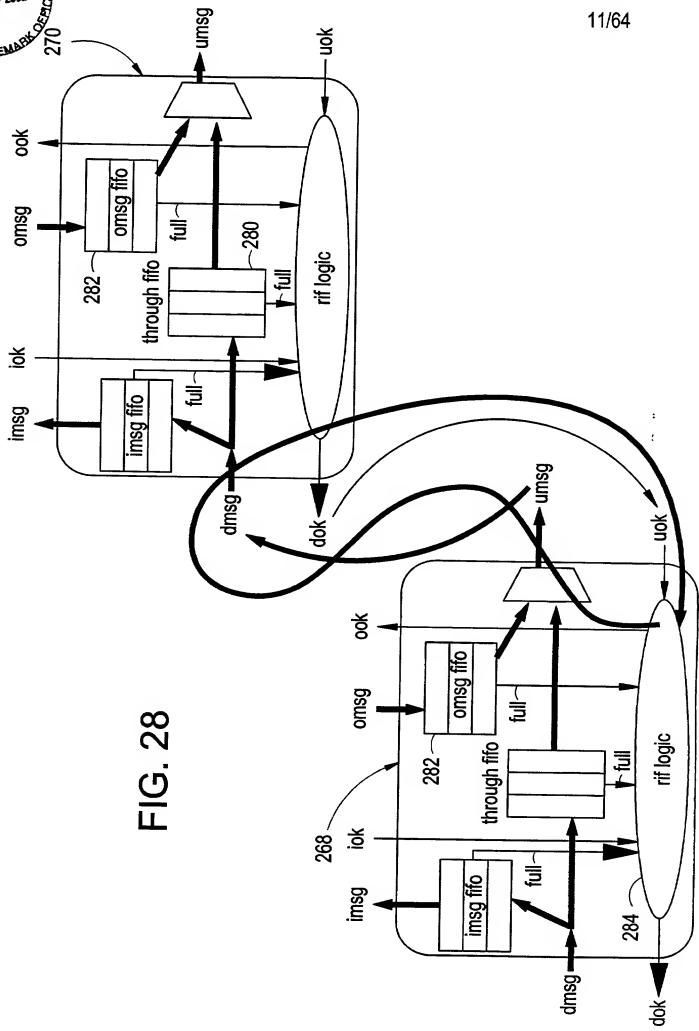


FIG. 27



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FIG. 28



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FIG. 29

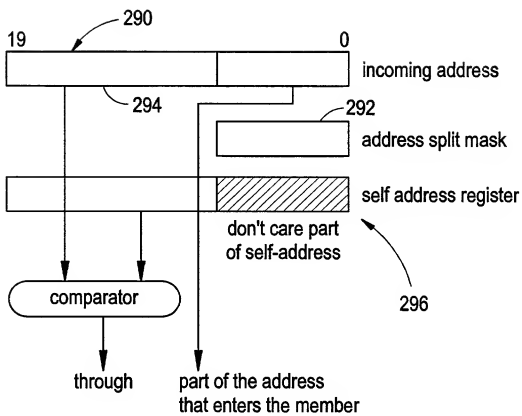


FIG. 30

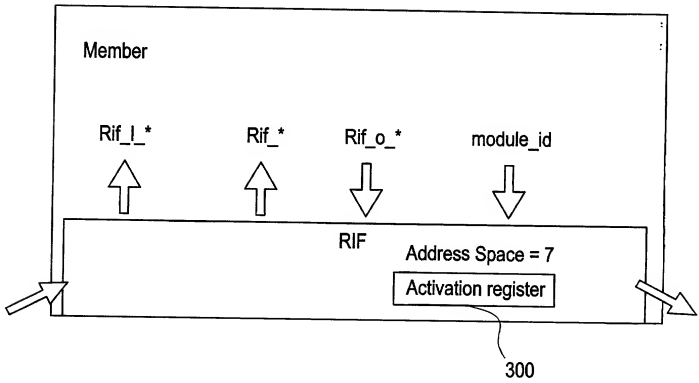
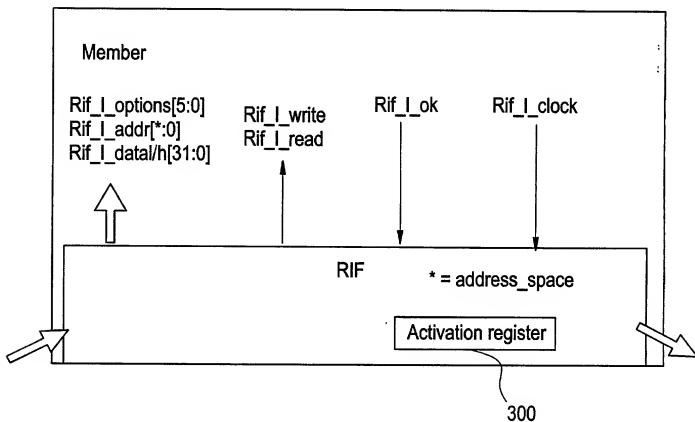


FIG. 31



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FIG. 32

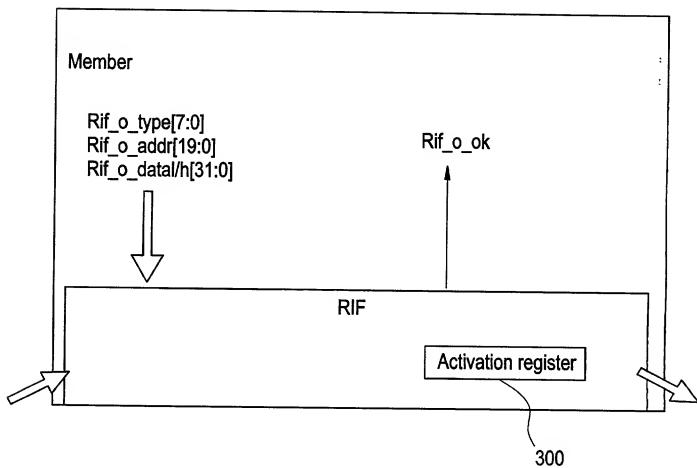
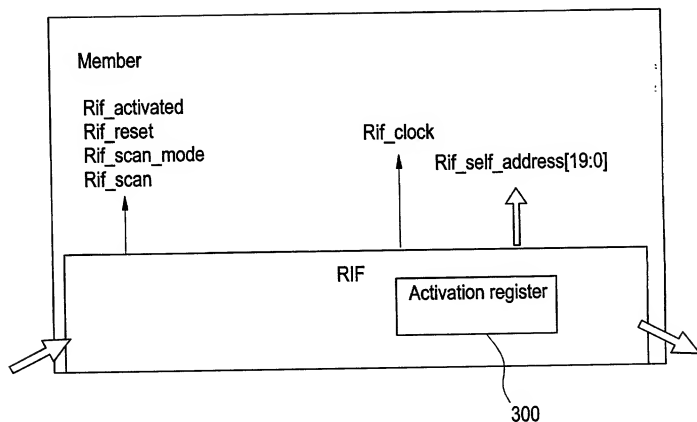


FIG. 33



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FIG. 34

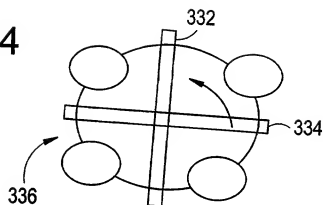


FIG. 35

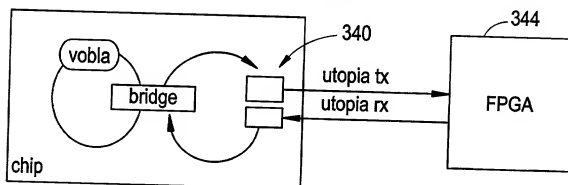


FIG. 36

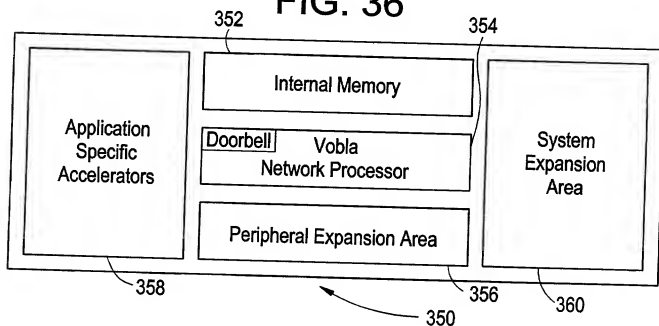
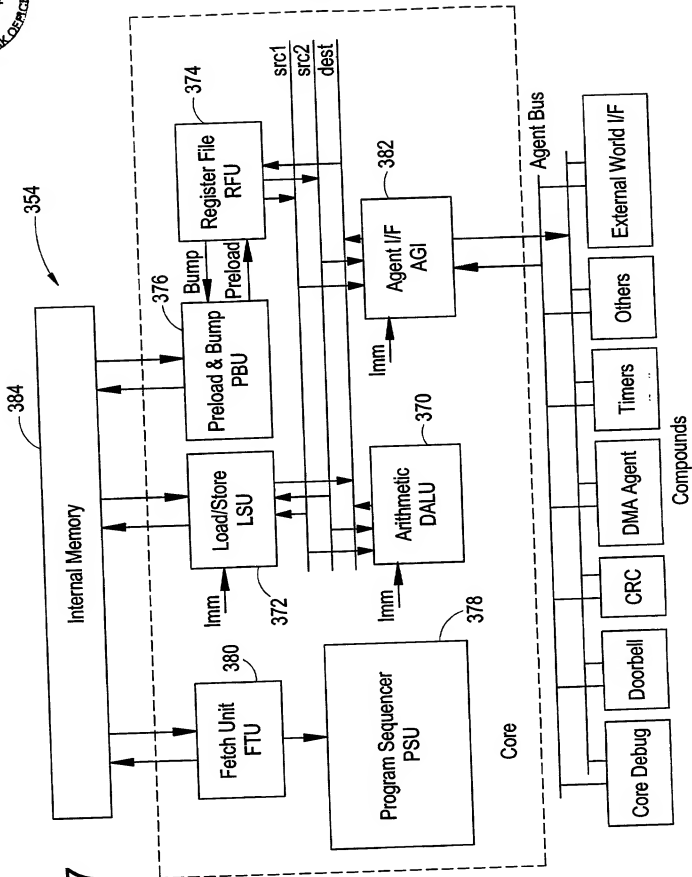


FIG. 37



209260-6249001

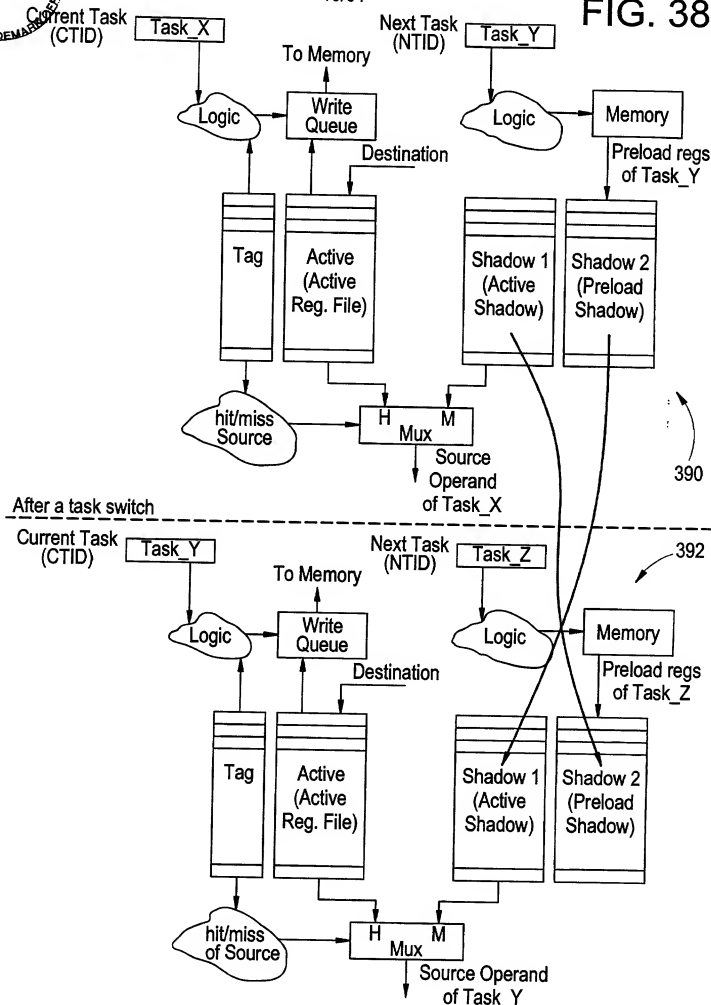


FIG. 39

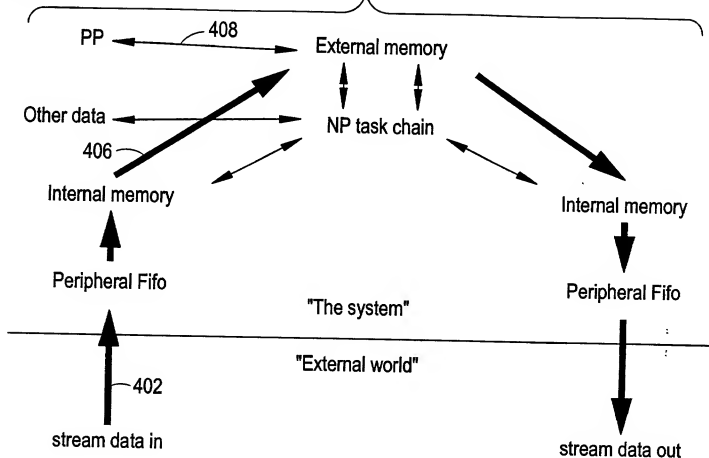


FIG. 40

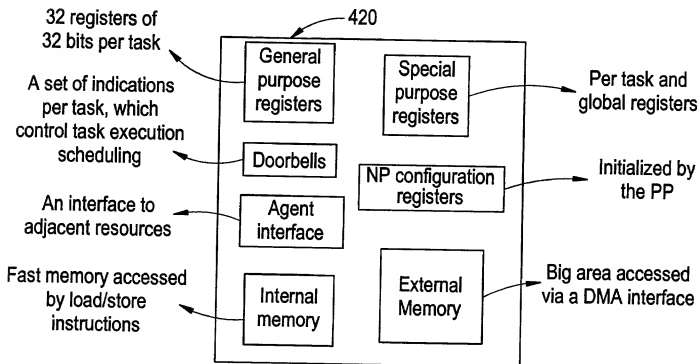
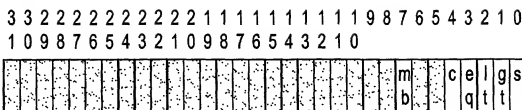


FIG. 41

R1 register



s - sticky bit  
 eq - equal/zero  
 lt - less then/negative  
 gt - greater then/positive  
 c - carry  
 mb - reflection of the RAM mult-reader busy indication

430

FIG. 42

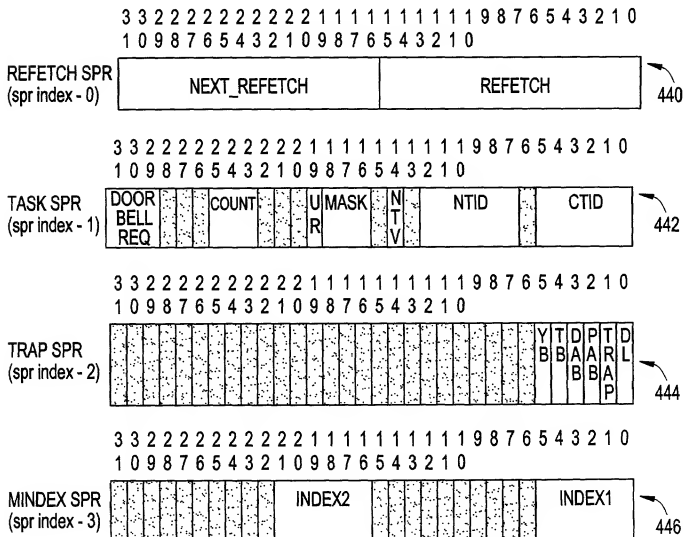
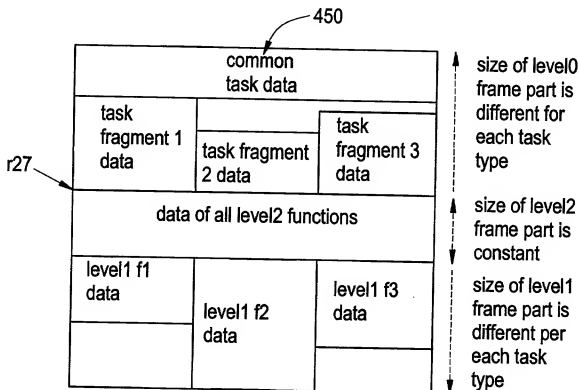


FIG. 43



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**FIG. 44**

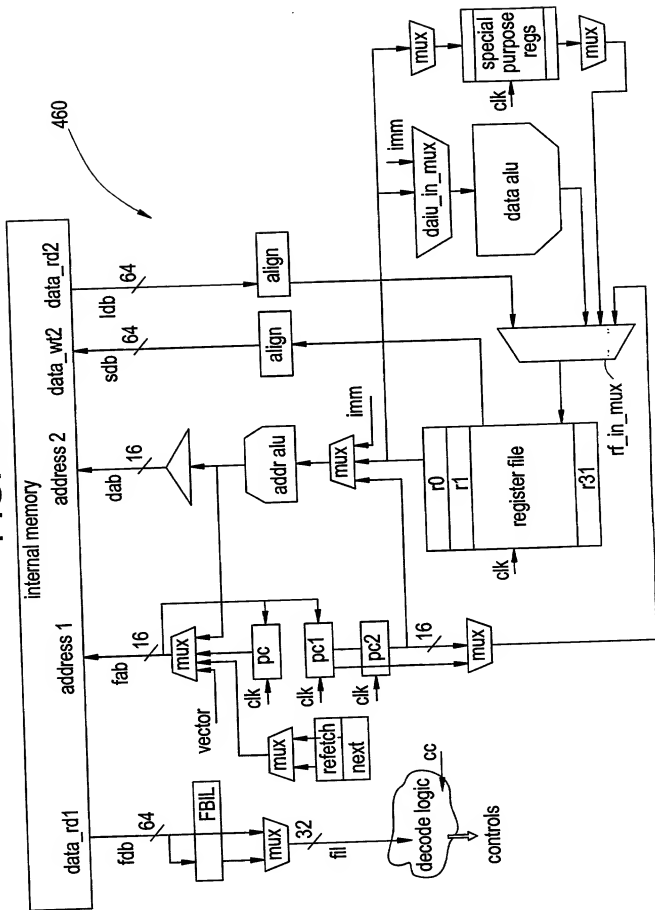
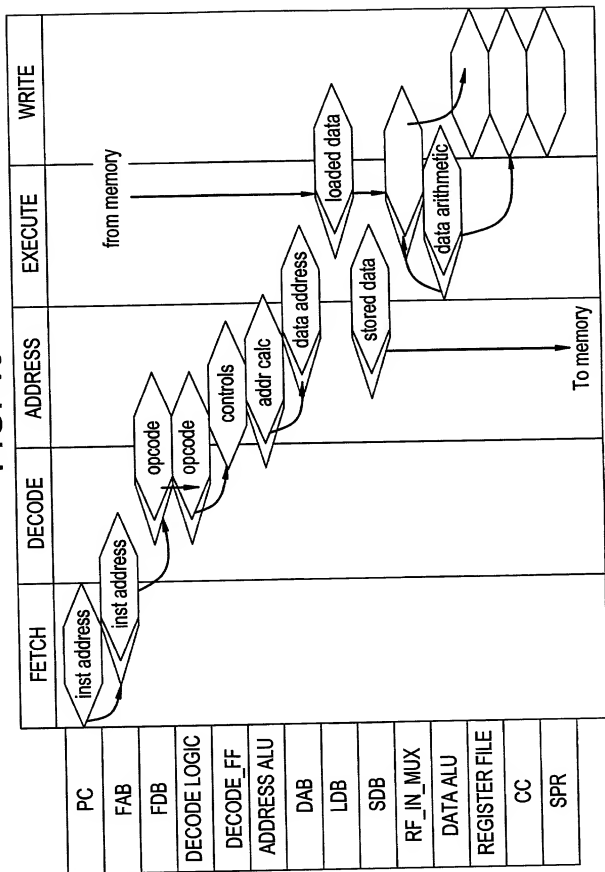


FIG. 45



480

- Logic

- Flip Flop



FIG. 46

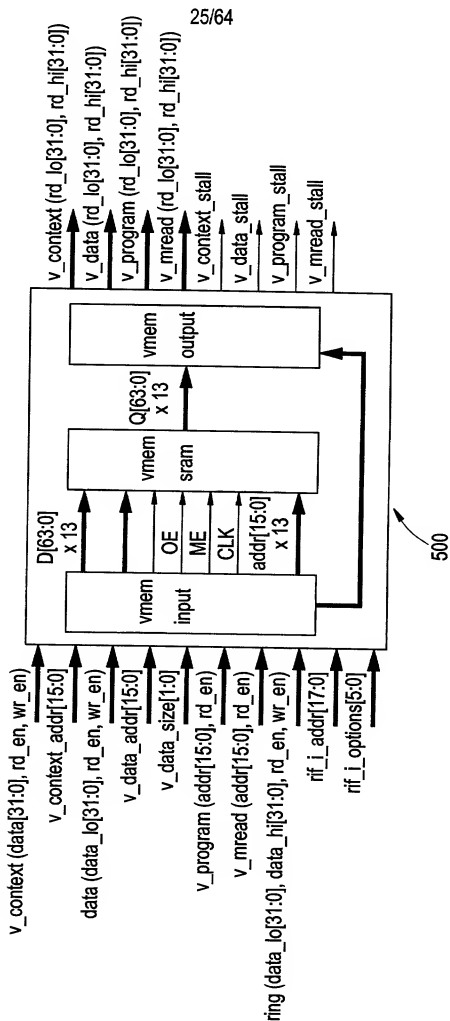


FIG. 47

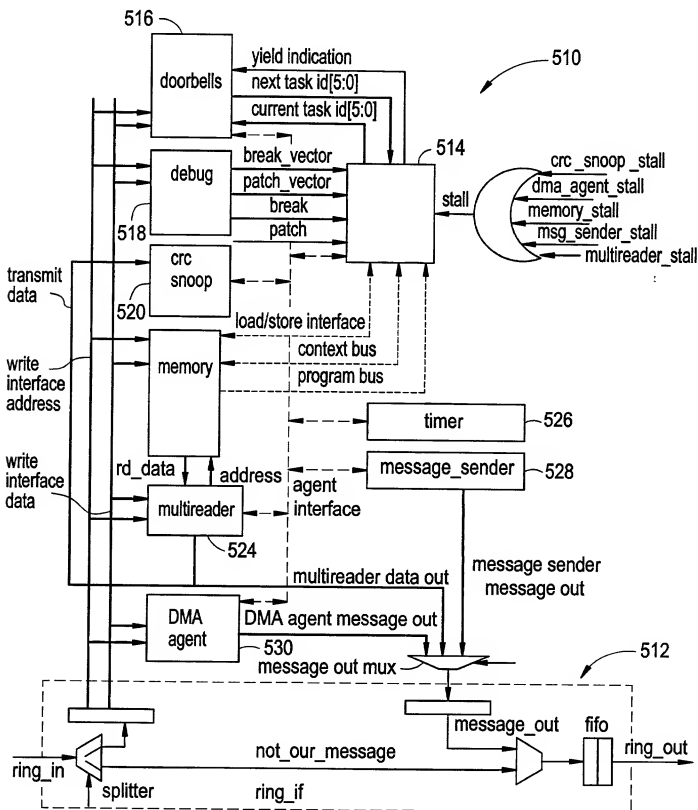


FIG. 48

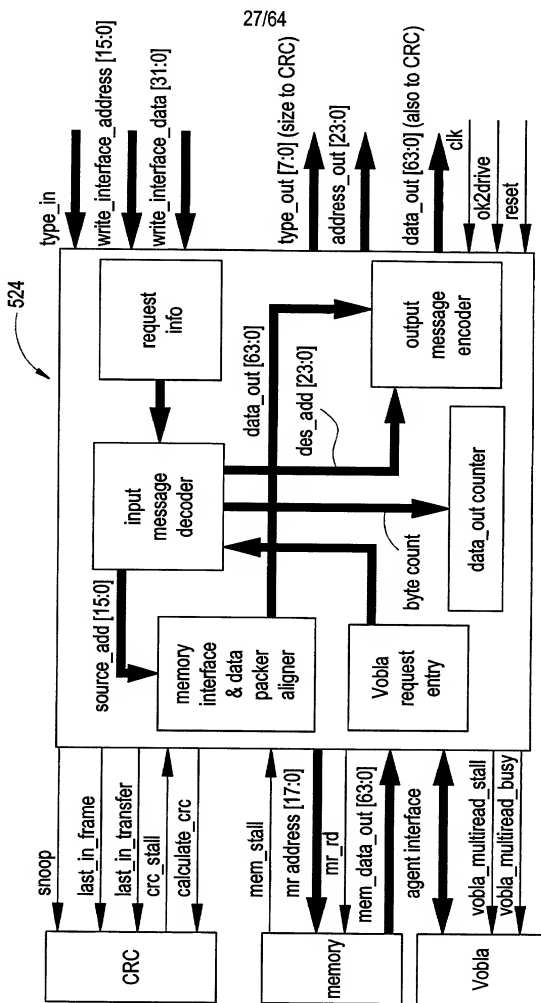


FIG. 49

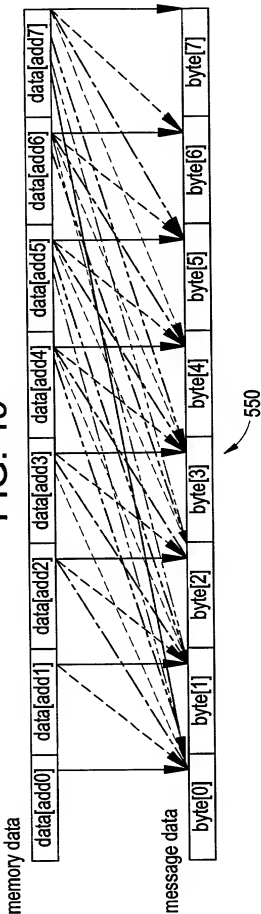


FIG. 50

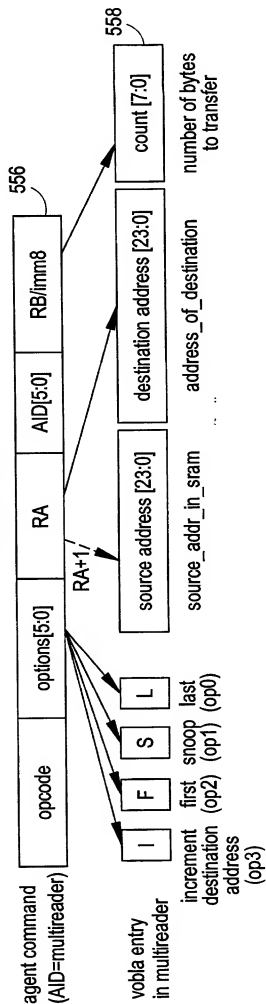


FIG. 51

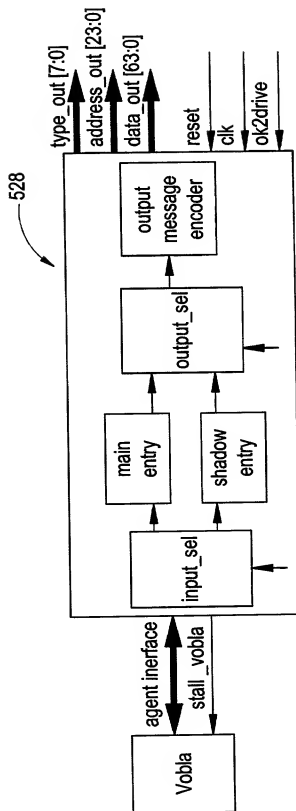


FIG. 52

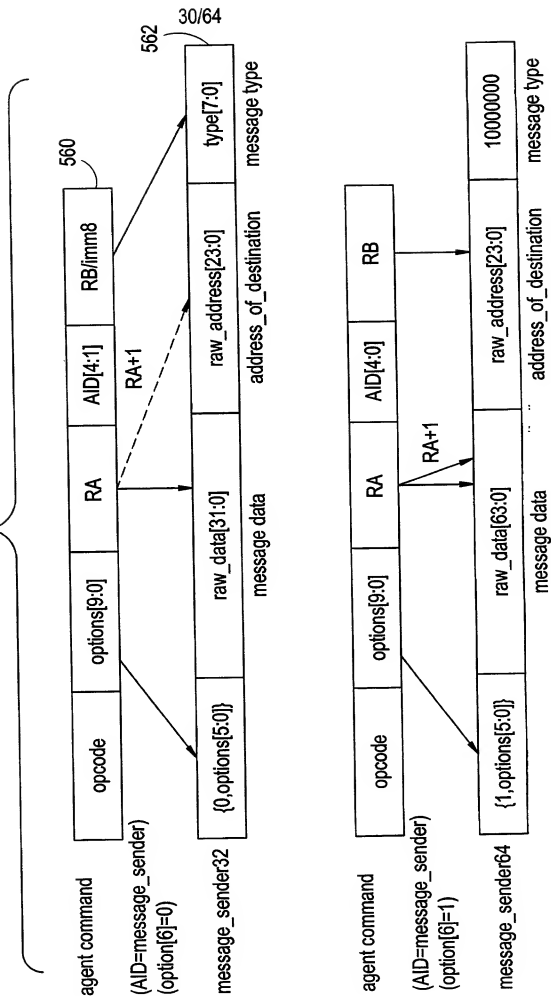


FIG. 53

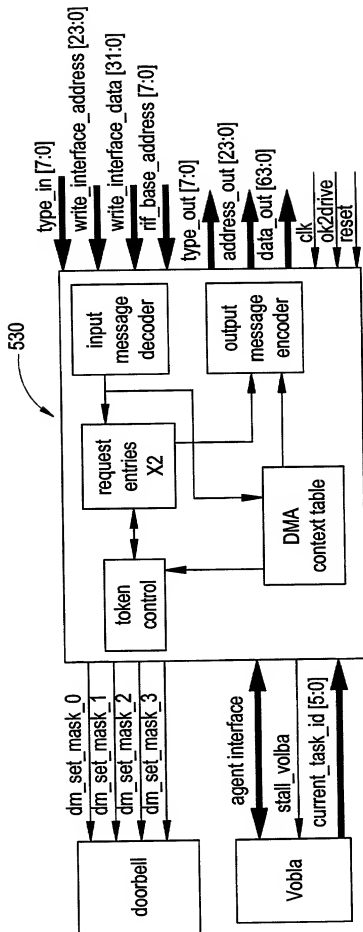


FIG. 54

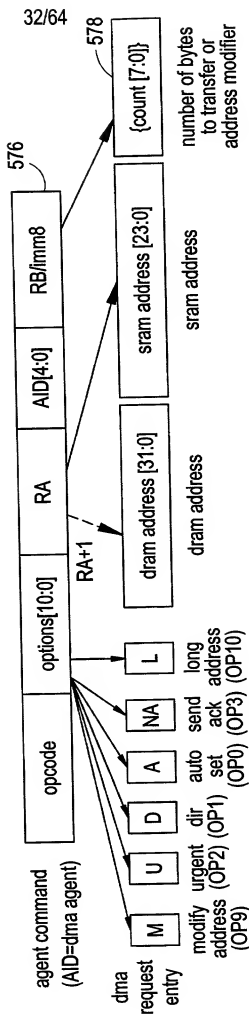




FIG. 55

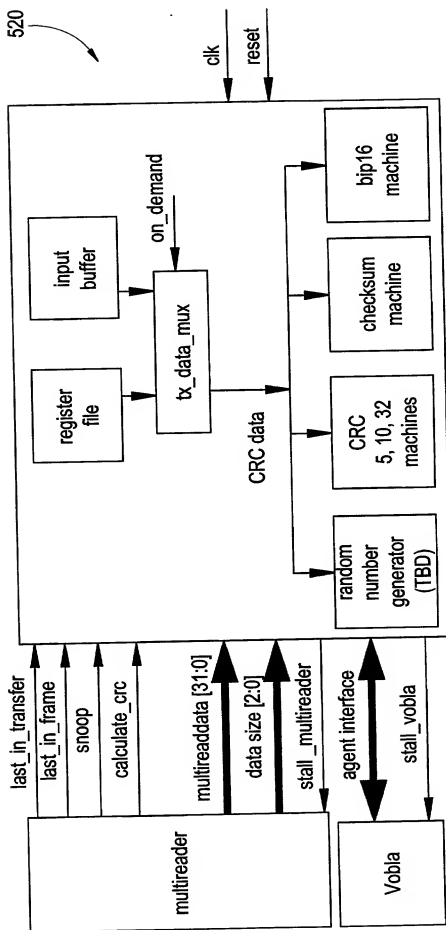


FIG. 56

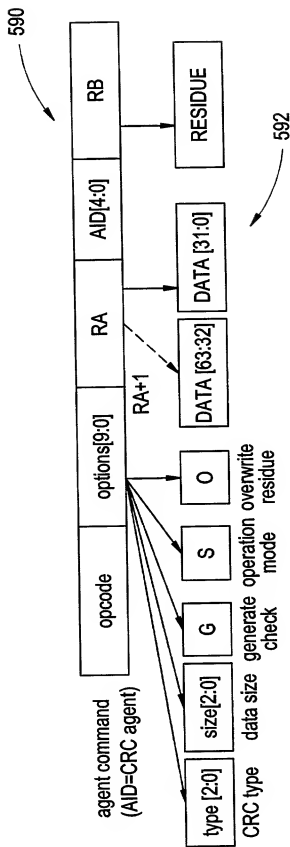


FIG. 57

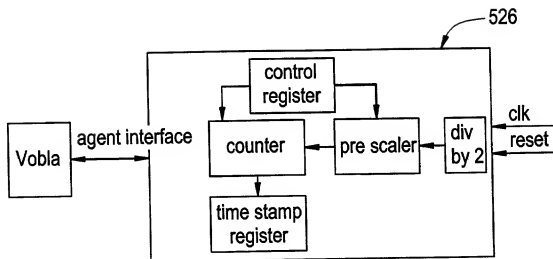


FIG. 58

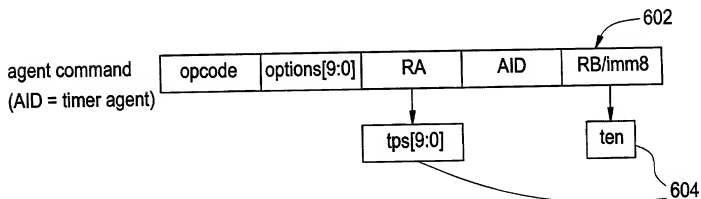


FIG. 59

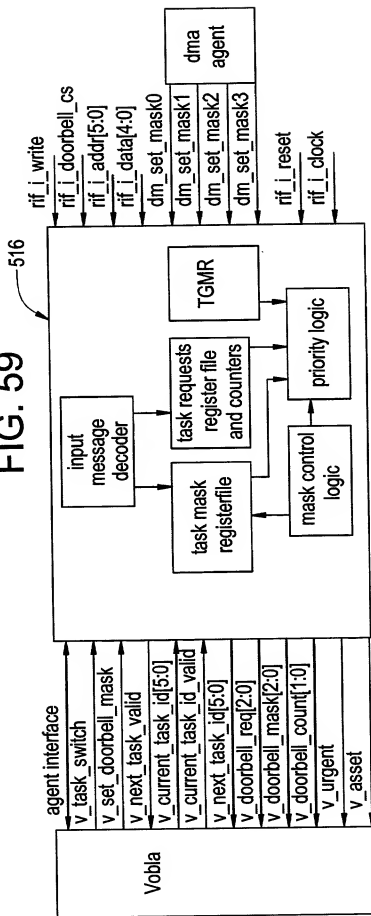


FIG. 60

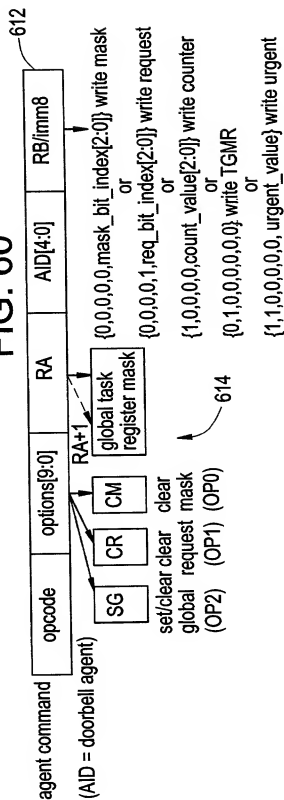


FIG. 61

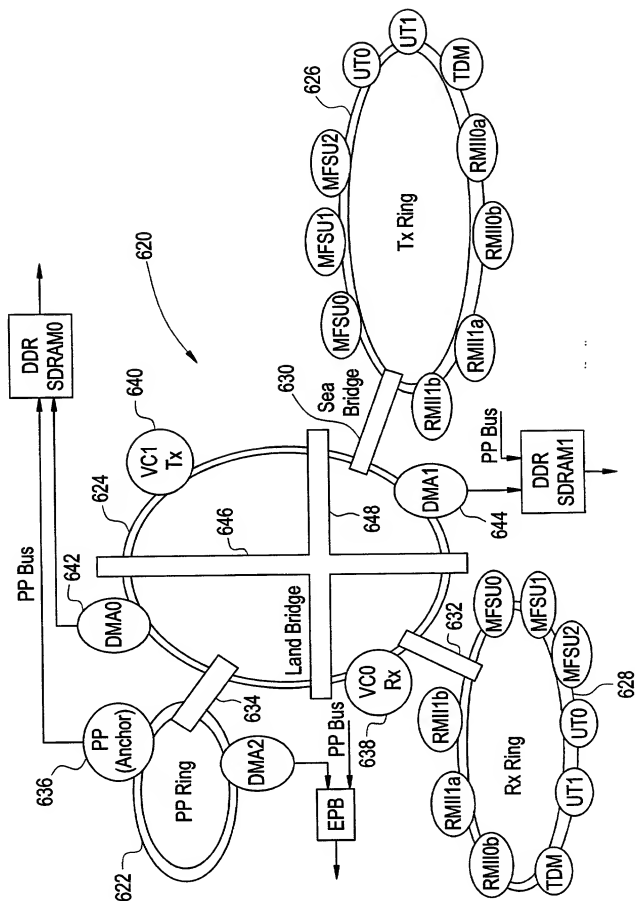


FIG. 62

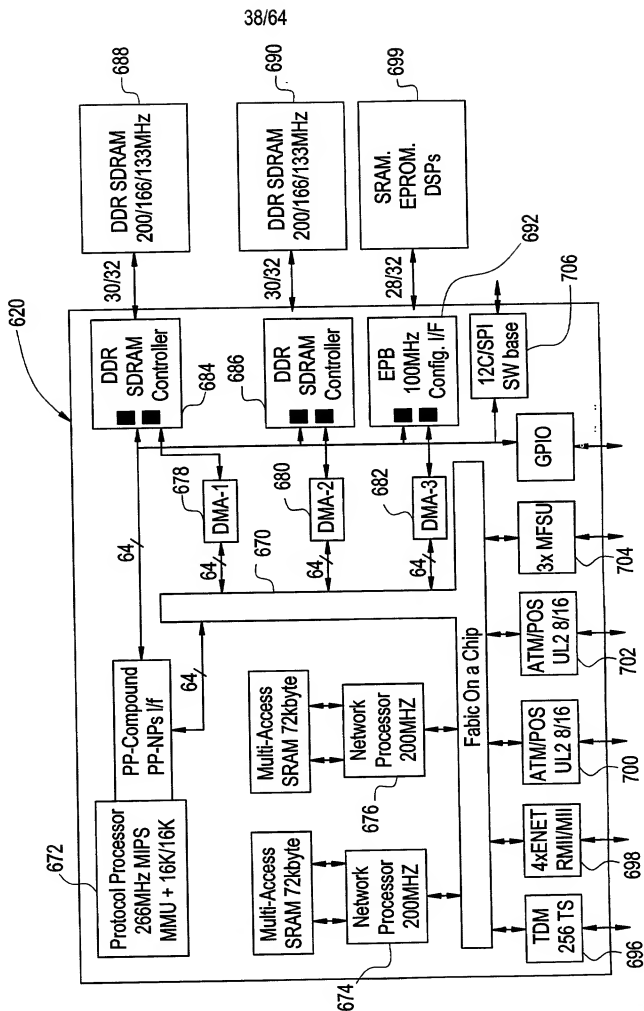
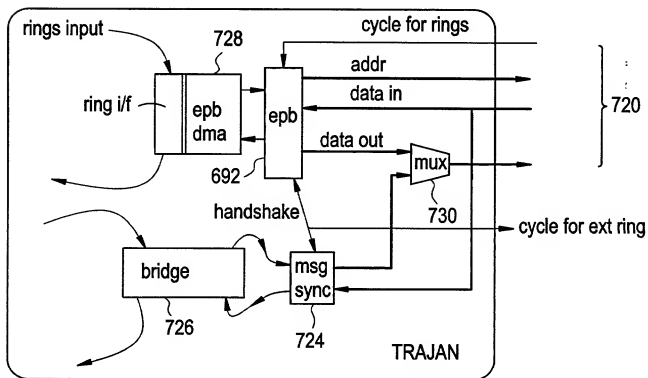
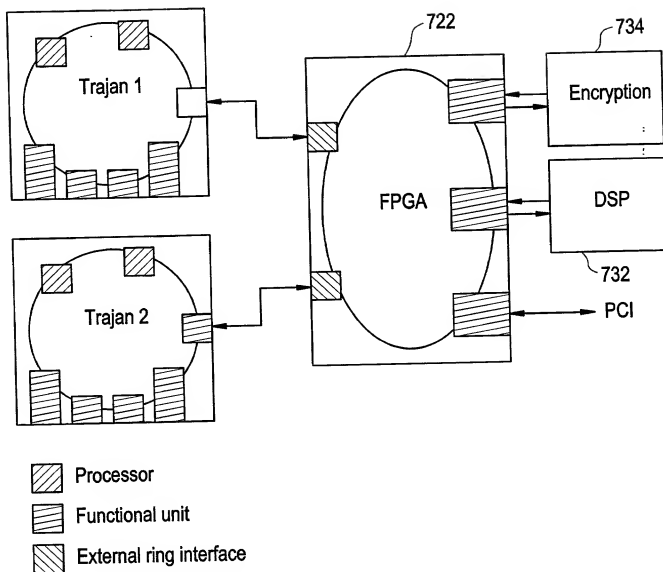


FIG. 63



10064229.092002

FIG. 64



1064729-002602



FIG. 65

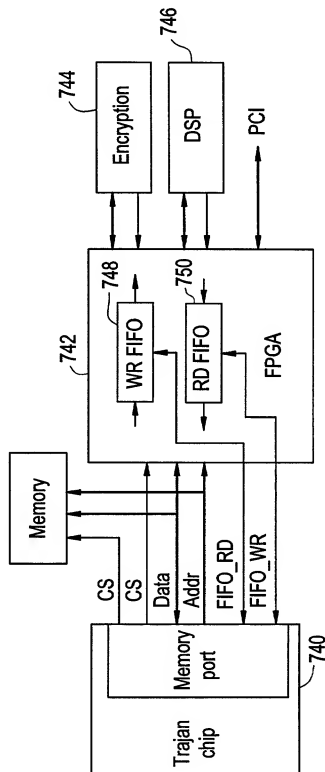


FIG. 66

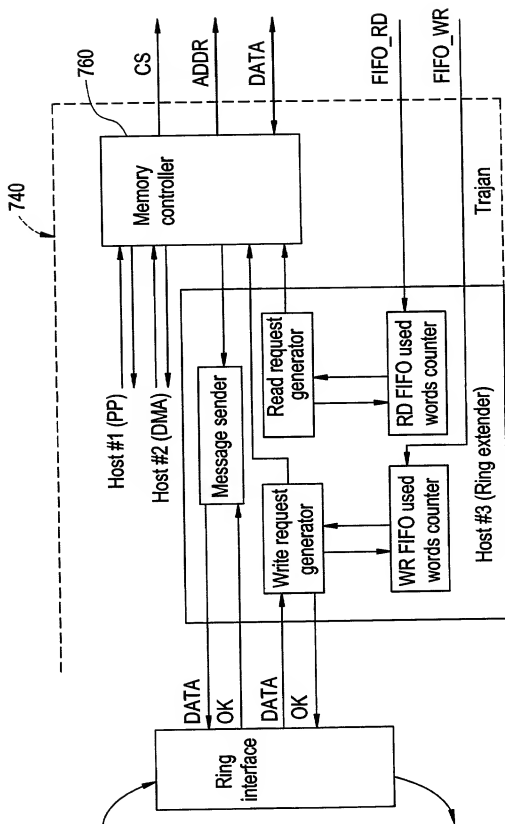


FIG. 67

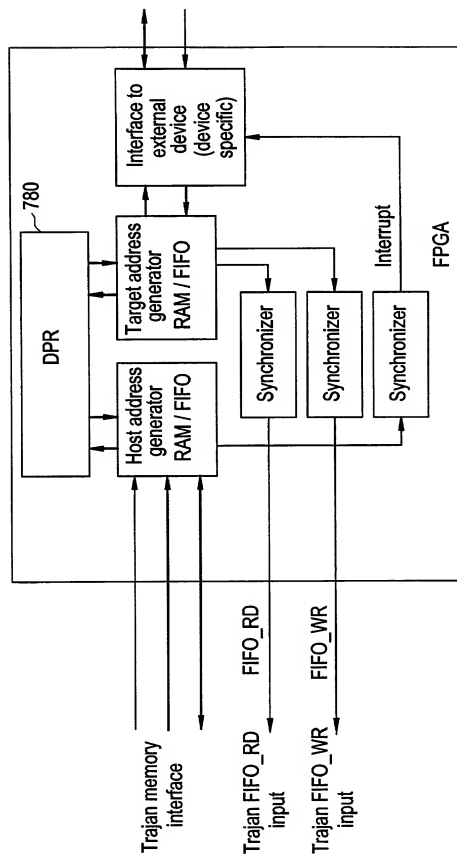


FIG. 68

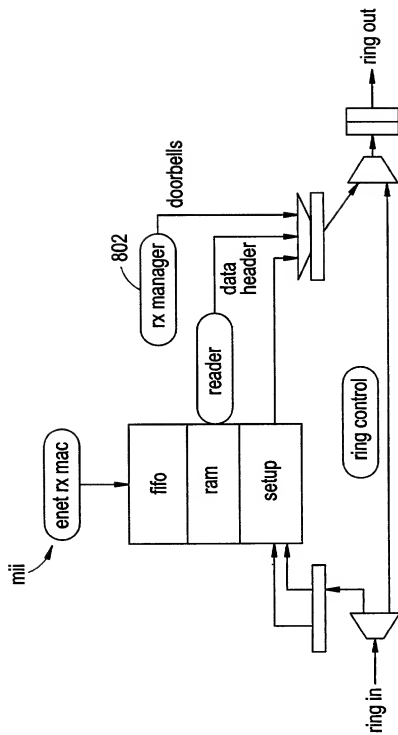


FIG. 69

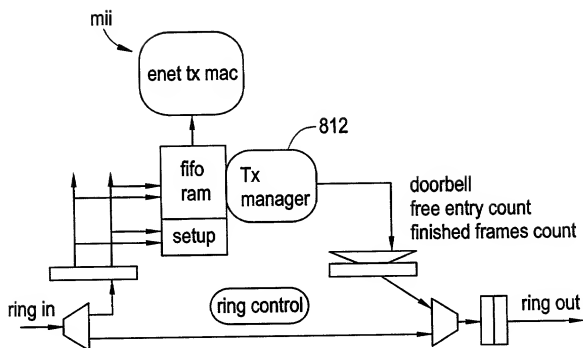
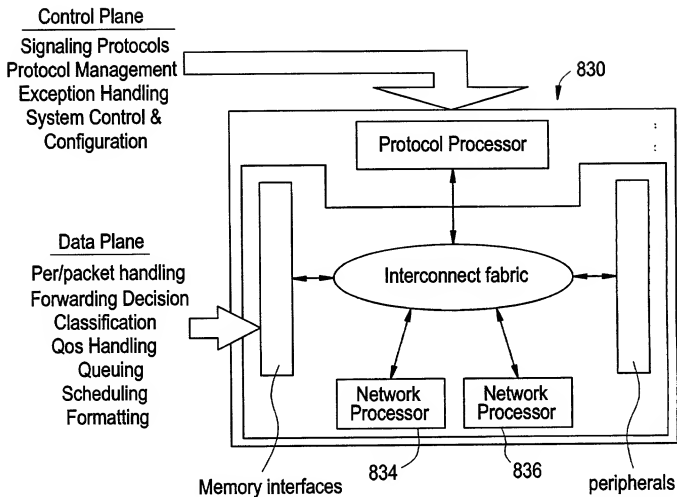


FIG. 70



10064329.092602

FIG. 71

840

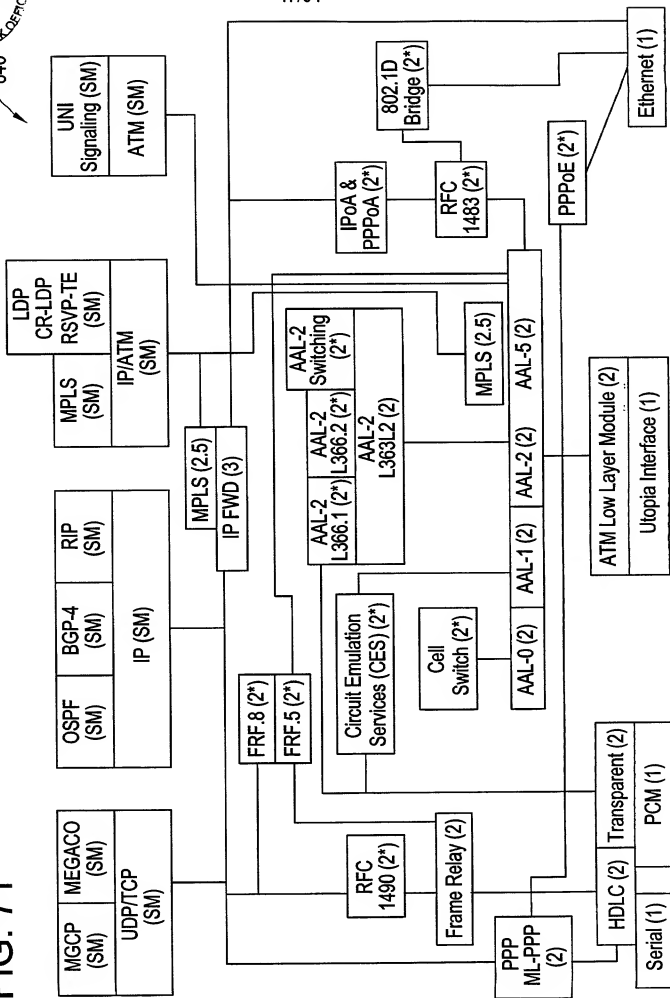


FIG. 72

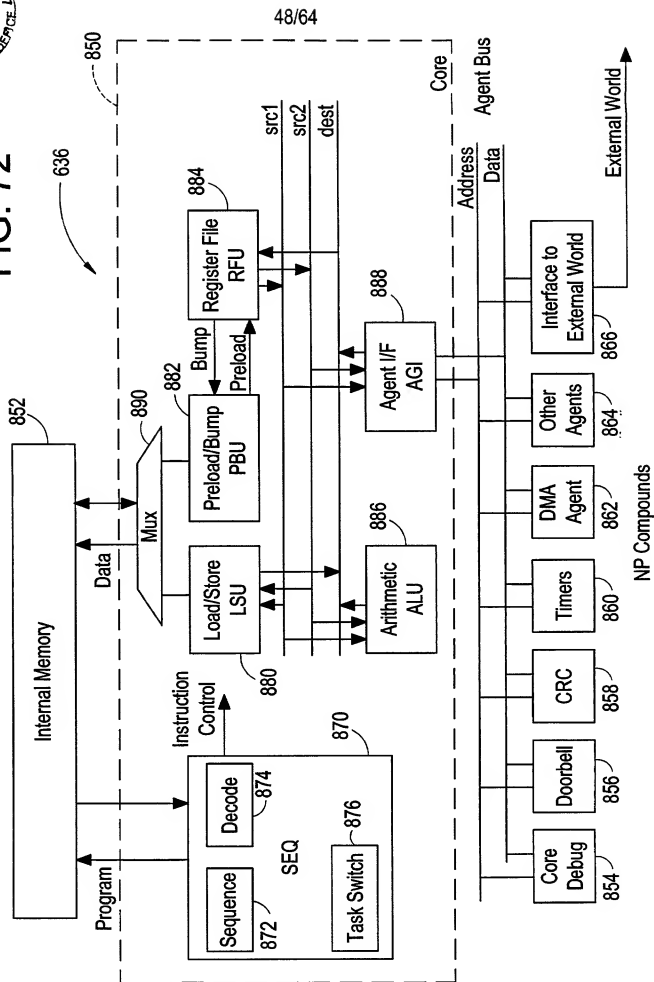




FIG. 73

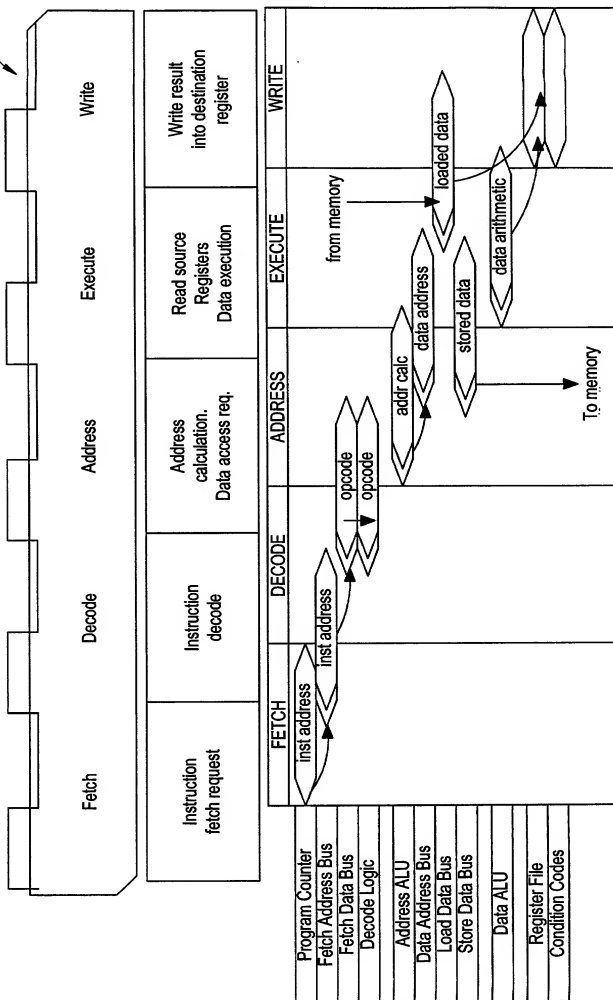


FIG. 74

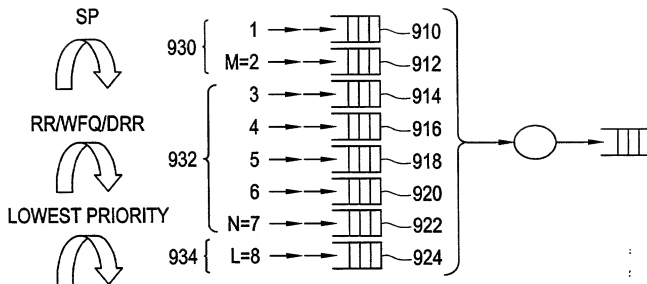
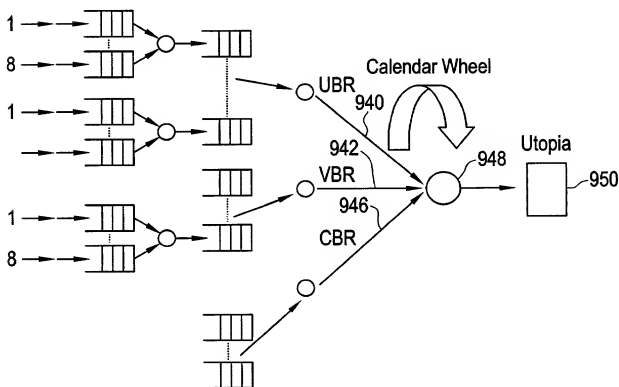


FIG. 75



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FIG. 76

960

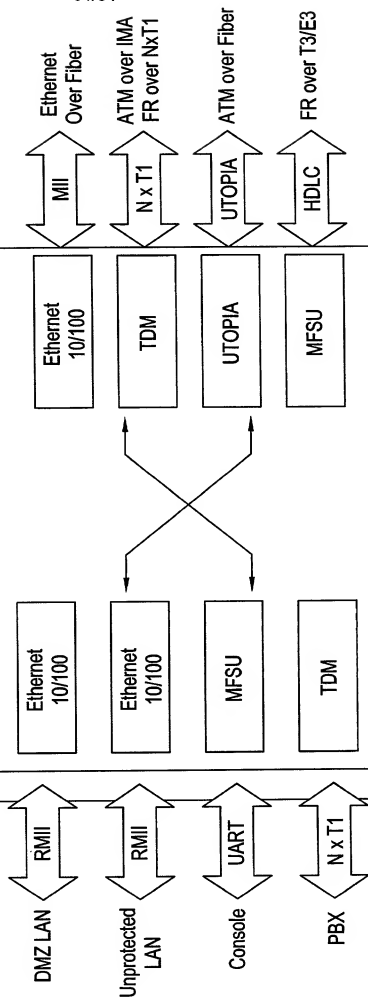
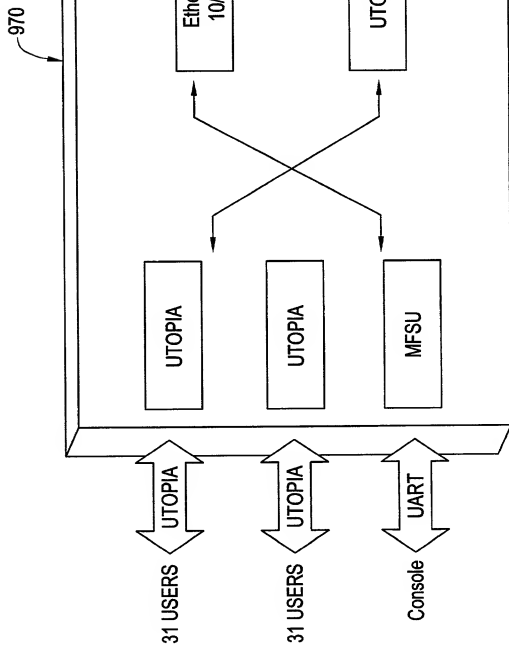


FIG. 77



209260-62E+9001

FIG. 78

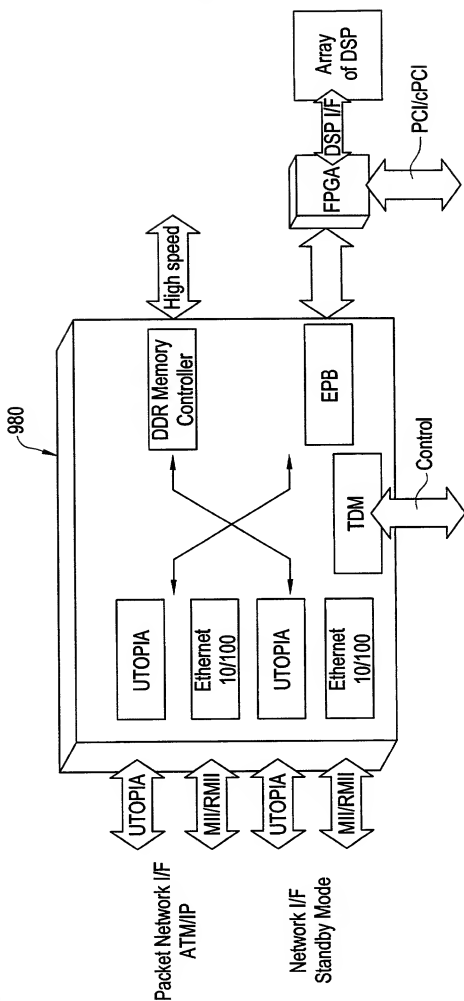


FIG. 79

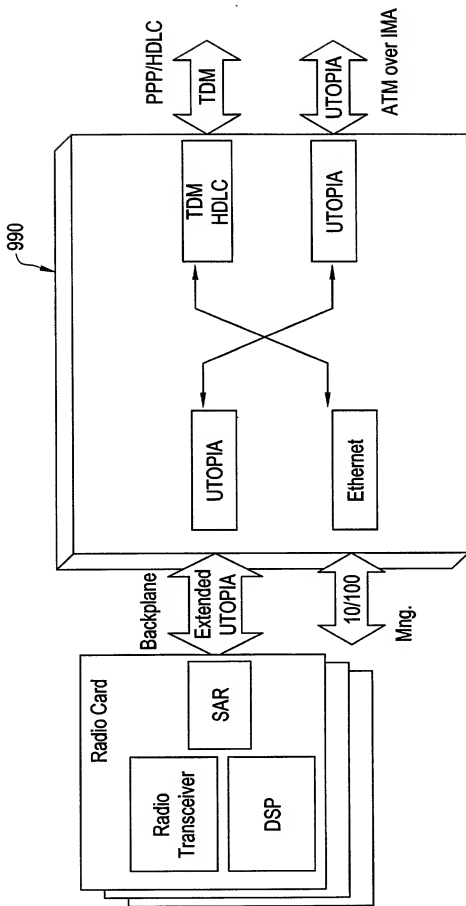


FIG. 80



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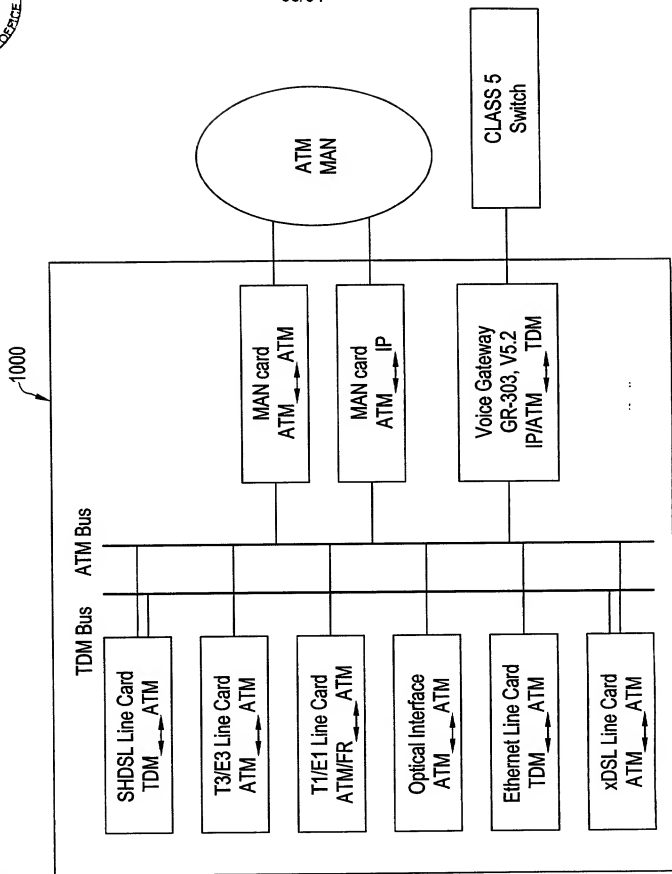


FIG. 81

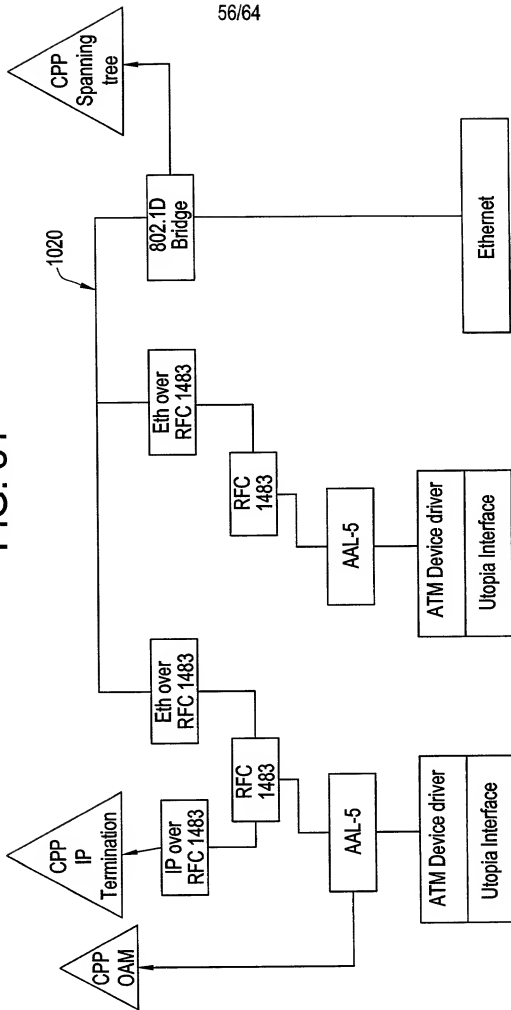




FIG. 82

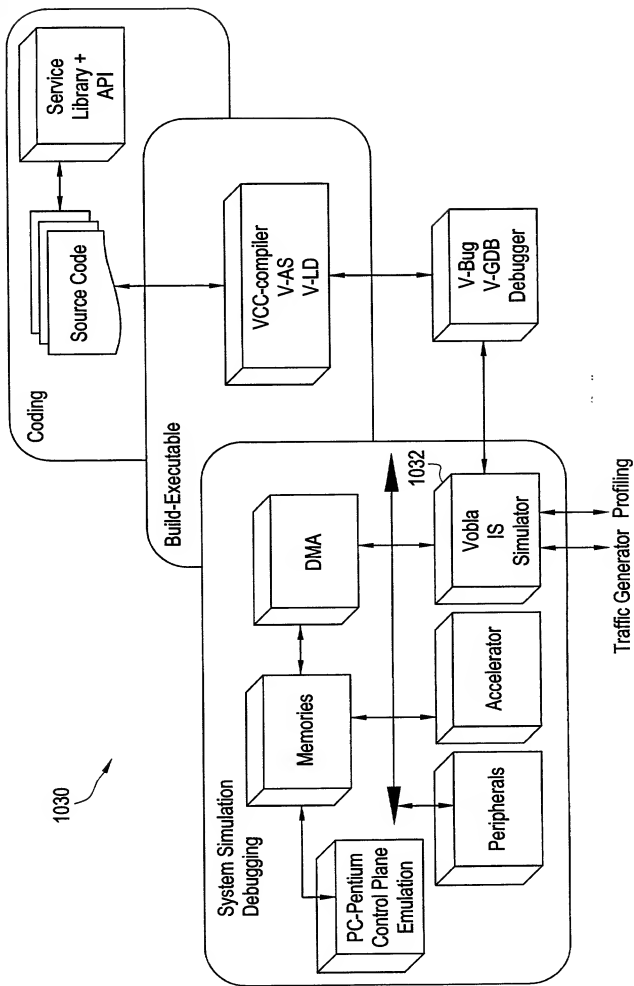


FIG. 83

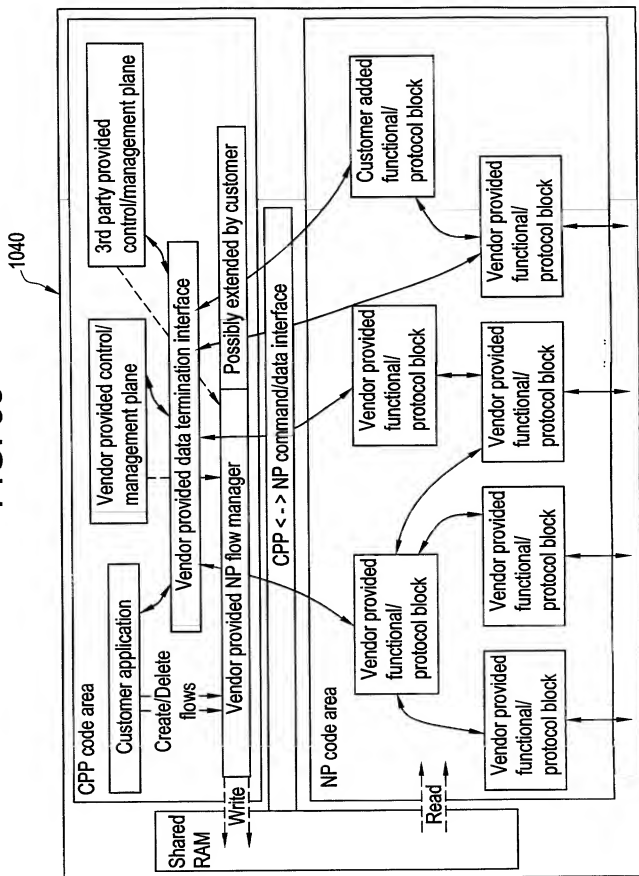


FIG. 84

1050

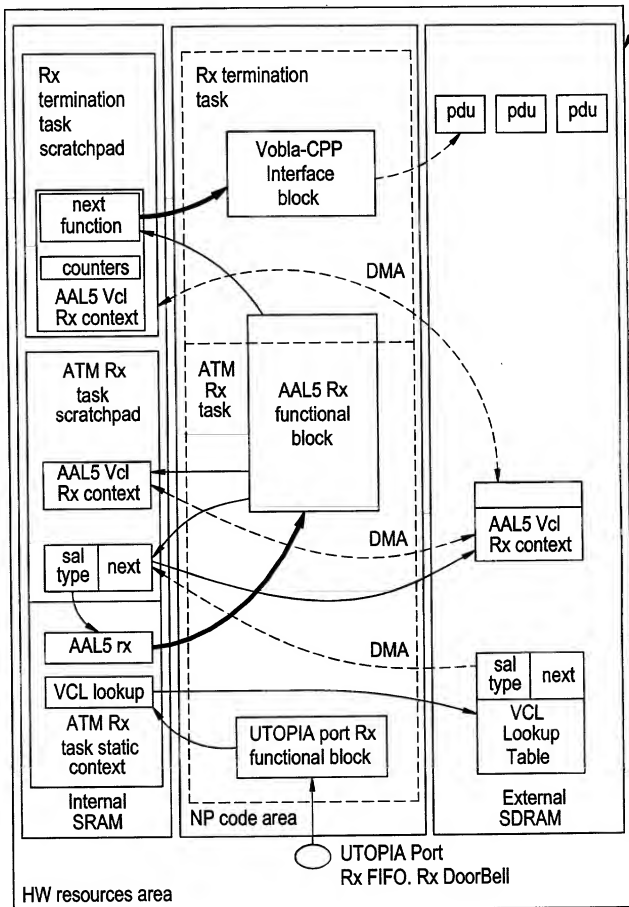


FIG. 85

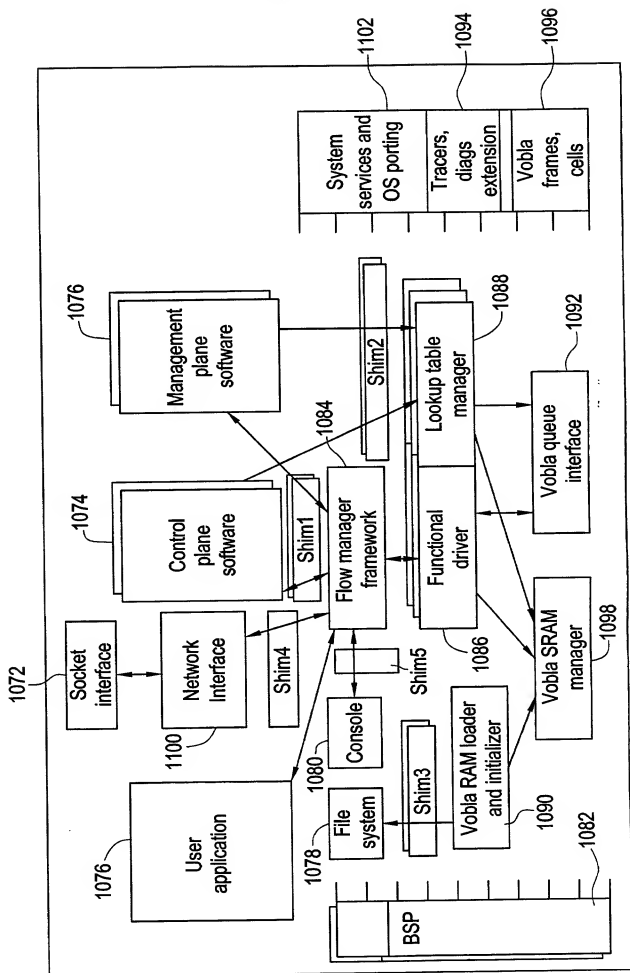
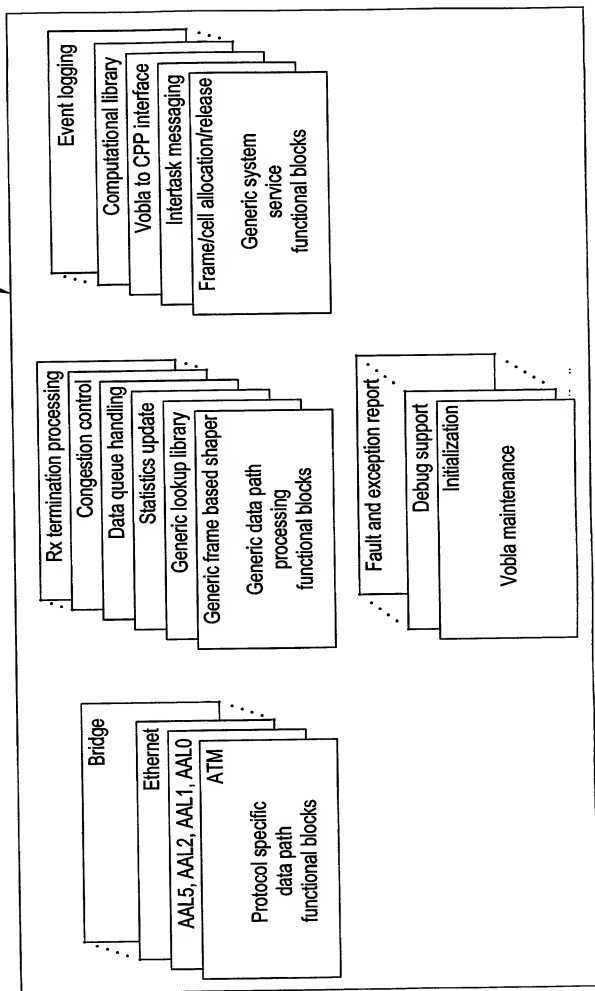


FIG. 86

1200



# FIG. 87

## PRIOR ART

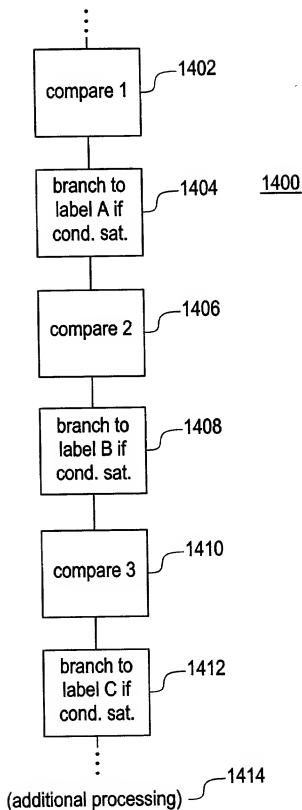


FIG. 88

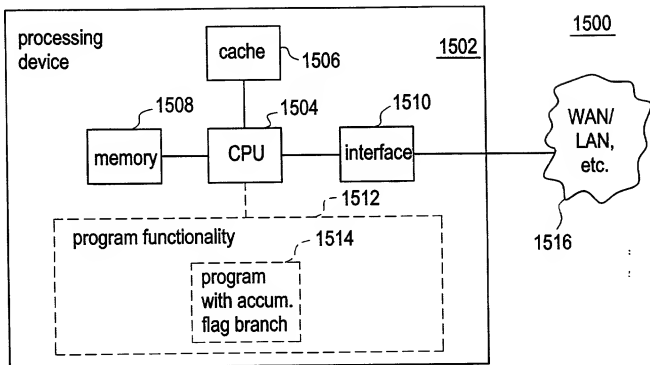
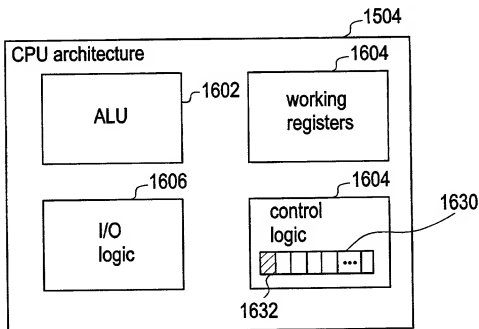


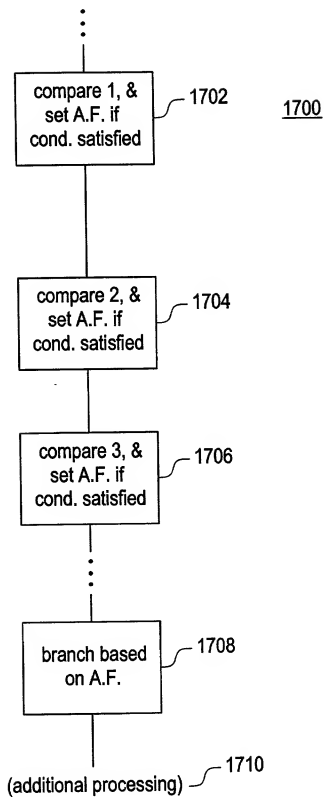
FIG. 89





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FIG. 90



10064329.092602